

**The Design Of High-speed Dynamic CMOS Circuits For
VLSI (Series In Microelectronics)
By Robert Rogenmoser**



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http://dblp.l3s.uni-hannover.de/?q=CMOS&search_opt=all&newQuery=yes&resTableName=query_resulta2HBoL&resultsPerPage=100

Modeling a High-Speed Inverter Using Cognitive the analysis and design of CMOS ingrated circuits; te energy-efficient VLSI design; 4)

<http://www.scirp.org/journal/PaperDownload.aspx?DOI=10.4236/wjet.2014.24031>

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Design of Low Voltage and High Speed Double-Tail Dynamic Comparator for Low Power Applications

http://www.academia.edu/7483629/Design_of_Low_Voltage_and_High_Speed_Double-Tail_Dynamic_Comparator_for_Low_Power_Applications

Design of High Speed and Low Offset Dynamic Latch Comparator in 0.18 m CMOS Process. Labonah Farzana Rahman ,

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power amplifier design High-speed serial I/O ULTRALOW-VOLTAGE DESIGN OF NANOMETER CMOS CIRCUITS FOR SMART (very large scale integration)

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Berkeley Wireless Research Center This research project focuses on the design of high output power CMOS Pursuing both high sampling speed and high dynamic

https://buffy.eecs.berkeley.edu/PHP/resabs/resabs.php?f_year=2007&f_submit=chapgrp&f_chapter=4

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High Speed Dynamic Comparator For High Speed ADCs: Design of a Novel High Speed Dynamic Comparator with Low Power Dissipation for High Speed ADCs

<http://www.amazon.com/High-Speed-Dynamic-Comparator-ADCs/dp/3659230723>

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"particularly suited to the newly developing integrated circuit design 7400 series parts, but uses CMOS circuits to speed up the low-to-high

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Tools and links for RF/analog circuit design and analysis. High-Speed CMOS Analog-To-Digital Design of Analog-Digital VLSI Circuits for Telecommunications and

http://www.circuitsage.com/circuit_design

Testing of static and dynamic circuits. Residue number system and high speed filters using RNS. low power VLSI CMOS circuit design.

<http://www.nitt.edu/home/academics/curriculum/MTech-ECE-VLSI-2013.pdf>

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<http://www.wikicfp.com/cfp/program?id=2957&f=International%20Conference%20on%20VLSI%20Design>

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<http://www.amazon.com/design-high-speed-dynamic-circuits-microelectronics/dp/3896490133>

A 3 V Wideband CMOS Switched-Current A/D-Converter Suitable for Time-Interleaved IEEE Circuits and Systems series A novel approach to high speed CMOS

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<https://groups.google.com/d/topic/sci.electronics.cad/ma8MQ-R5Nto>

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<https://groups.google.com/d/msg/sci.math.num-analysis/hpW7rg6Y9Oc/zCyokeLxJgQJ>

VLSI Anna University New Syllabus Low power CMOS VLSI circuit design system level crosstalk To design high speed Printed Circuit board with minimum

<https://www.scribd.com/doc/167827372/VLSI-Anna-University-New-Syllabus-2013>

we will provide our first outline of the very-large-scale integration (VLSI) design for Digital Circuit Design . and high-speed analog and mixed signal

<http://www.ncbi.nlm.nih.gov/pmc/articles/PMC3395724/>

Electronic Circuit Clocking; Circuit Design; High Performance Publications Publications Detection," IEEE Transactions on Very Large Scale Integration

https://faculty.utah.edu/u0456963-RICHARD_B_BROWN/bibliography/index.html

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